

NOV 25 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Satwant Singh; Cyrus Tsui  
Assignee: Lattice Semiconductor Corporation  
Title: Delay-Matched ASIC Conversion of a Programmable Logic Devices  
Serial No.: 10/660,814 Filing Date: September 12, 2003  
Examiner: Daniel D. Chang Group Art 2819  
Unit:  
Docket No.: M-15198 US

Irvine, California  
November 25, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action dated September 1, 2005, Applicants submit the following amendment and remarks.

LAW OFFICES OF  
MACPHERSON KWOK  
CHEN & HEID LLP

2402 Michelson Drive  
SUITE 210  
Irvine, CA 92612  
(949) 752-7040  
FAX (949) 752-7049

-1- Serial No. 10/660,814